

FIG. 1

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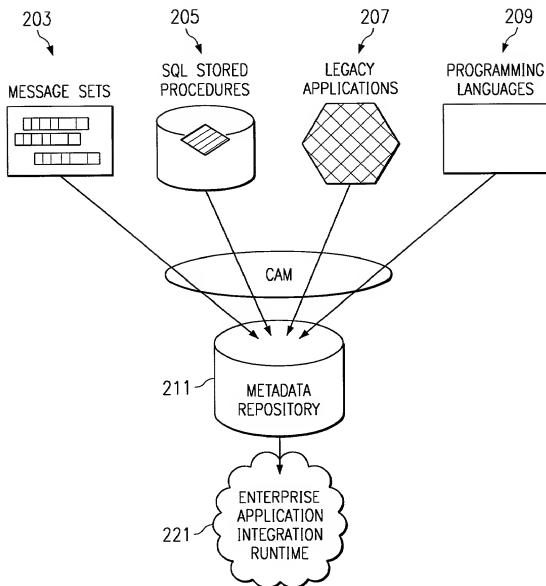


FIG. 2

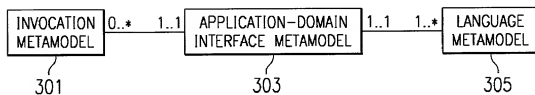
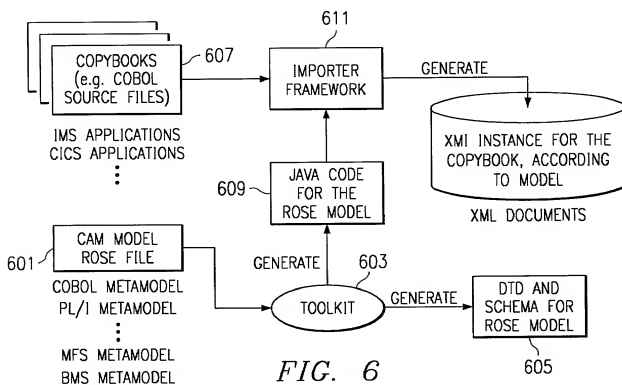
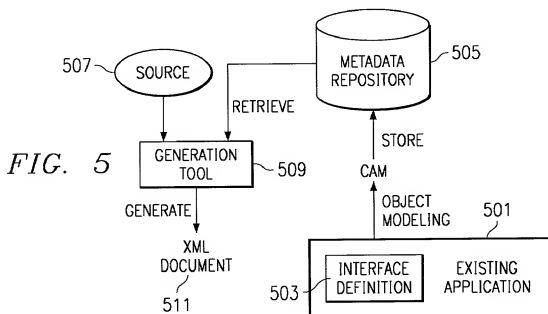
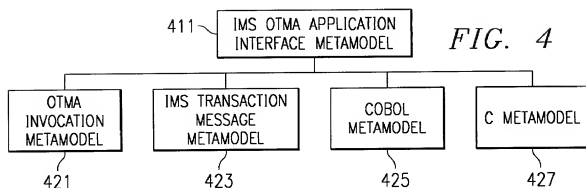


FIG. 3

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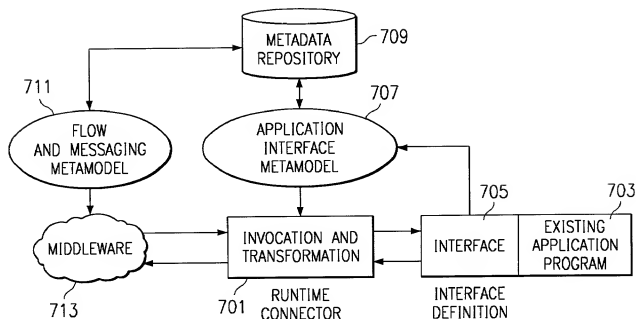


FIG. 7

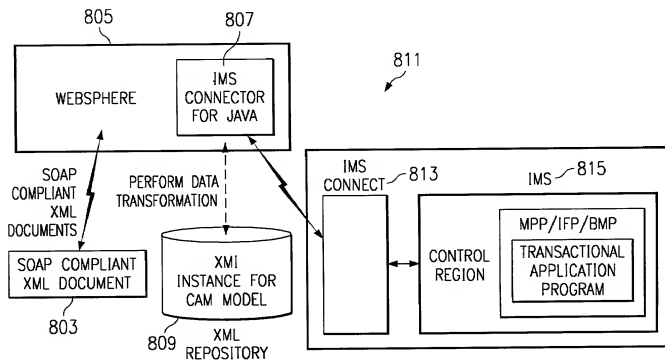


FIG. 8

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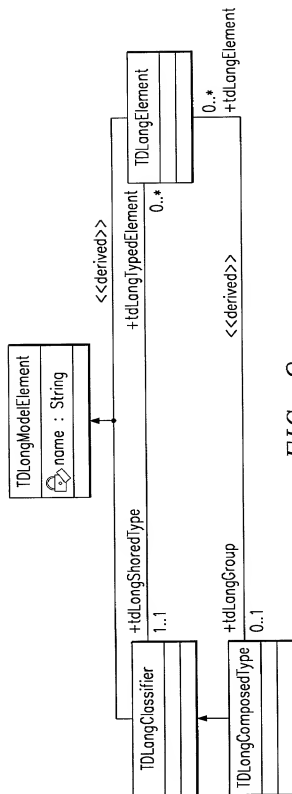


FIG. 9

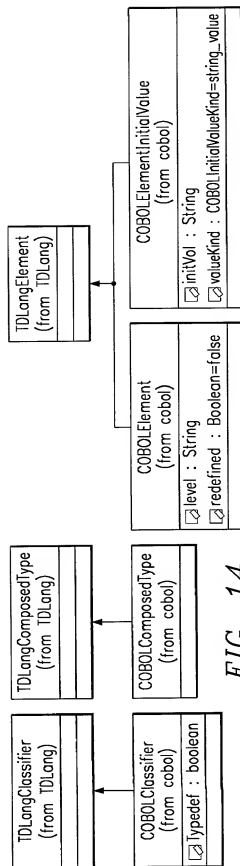
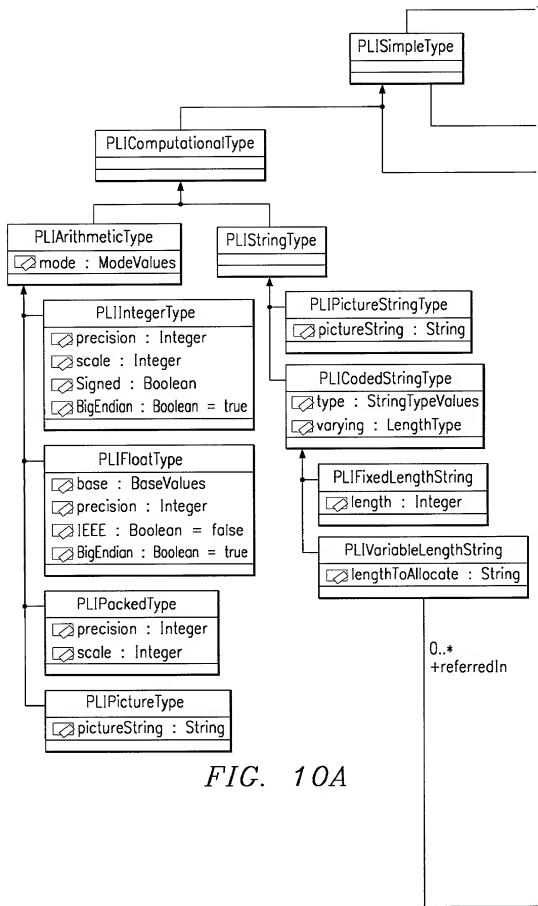


FIG. 14

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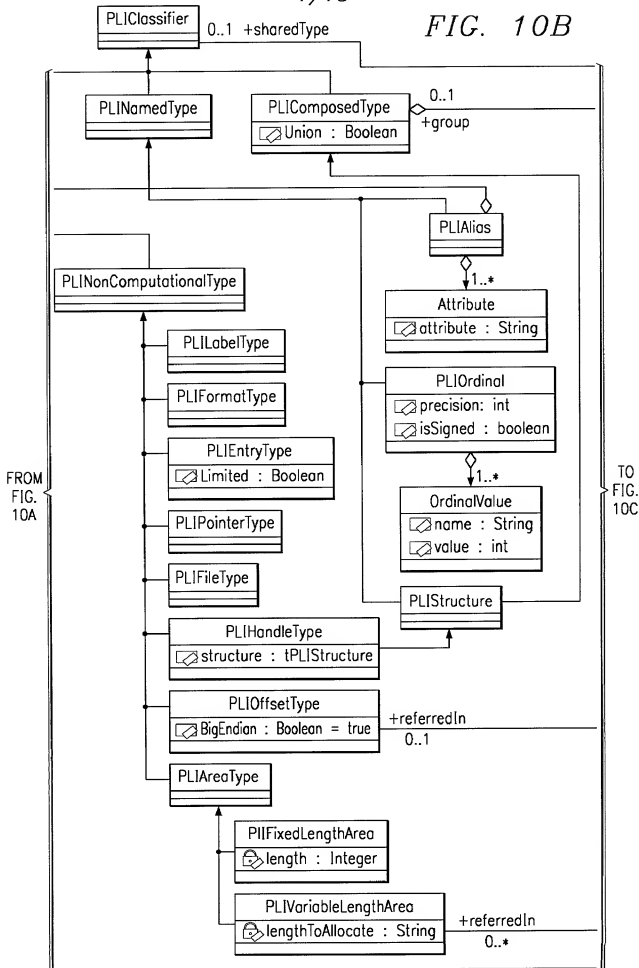


TO
FIG.
10B

FIG. 10A

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FIG. 10B



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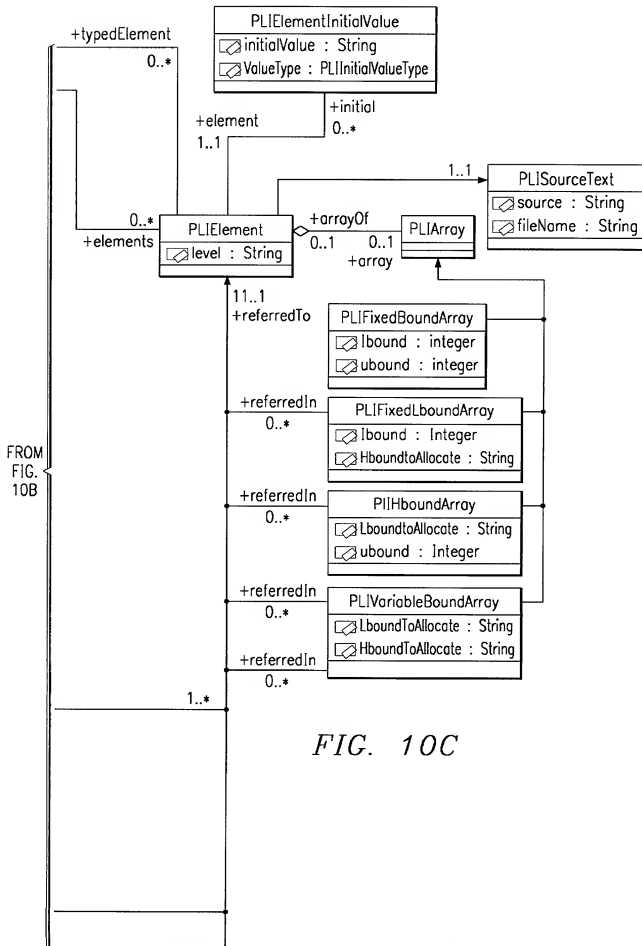


FIG. 10C

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FIG. 11

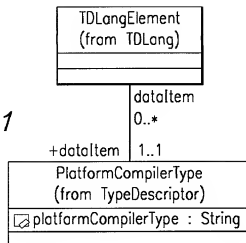


FIG. 15

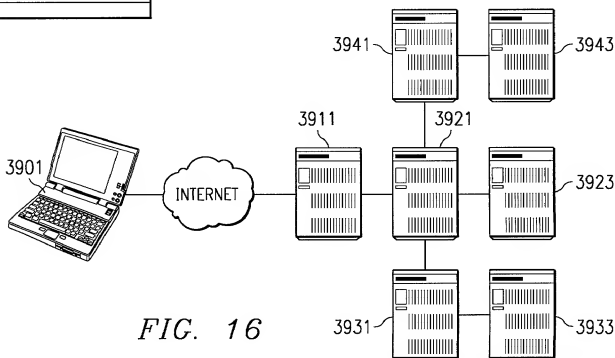
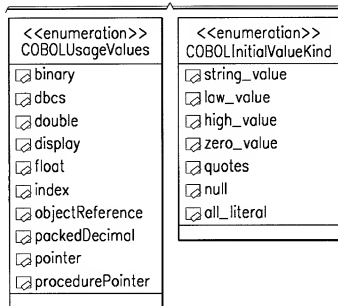


FIG. 16

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<<enumeration>> signCoding	<input checked="" type="checkbox"/> twosComplement <input checked="" type="checkbox"/> onesComplement <input checked="" type="checkbox"/> signMagnitude <input checked="" type="checkbox"/> zoneSigns <input checked="" type="checkbox"/> packedSigns <input checked="" type="checkbox"/> unsignedBinary <input checked="" type="checkbox"/> unsignedDecimal	<<enumeration>> lengthEncoding	<input checked="" type="checkbox"/> fixedLength <input checked="" type="checkbox"/> lengthPrefixed <input checked="" type="checkbox"/> nullTerminated	<<enumeration>> floatType	<input checked="" type="checkbox"/> Unspecified <input checked="" type="checkbox"/> IEEEExtendedIntel <input checked="" type="checkbox"/> IEEEExtendedAIX <input checked="" type="checkbox"/> IEEEExtendedOS390 <input checked="" type="checkbox"/> IEEEExtendedAS400 <input checked="" type="checkbox"/> IEEEnonextended <input checked="" type="checkbox"/> IBM390Hex <input checked="" type="checkbox"/> IBM400Hex	<<enumeration>> accessor	<input checked="" type="checkbox"/> ReadOnly <input checked="" type="checkbox"/> WriteOnly <input checked="" type="checkbox"/> ReadWrite <input checked="" type="checkbox"/> NoAccess	<<enumeration>> packedDecimalSign	<input checked="" type="checkbox"/> MVS <input checked="" type="checkbox"/> MVSCustom <input checked="" type="checkbox"/> NT_OS2_AIX	<<enumeration>> bitModePad	<input checked="" type="checkbox"/> mode16 <input checked="" type="checkbox"/> mode24 <input checked="" type="checkbox"/> mode31 <input checked="" type="checkbox"/> mode32 <input checked="" type="checkbox"/> mode64 <input checked="" type="checkbox"/> mode128
<<enumeration>> addrUnit	<input checked="" type="checkbox"/> bit <input checked="" type="checkbox"/> byte <input checked="" type="checkbox"/> word <input checked="" type="checkbox"/> doubleWord										

FIG. 12

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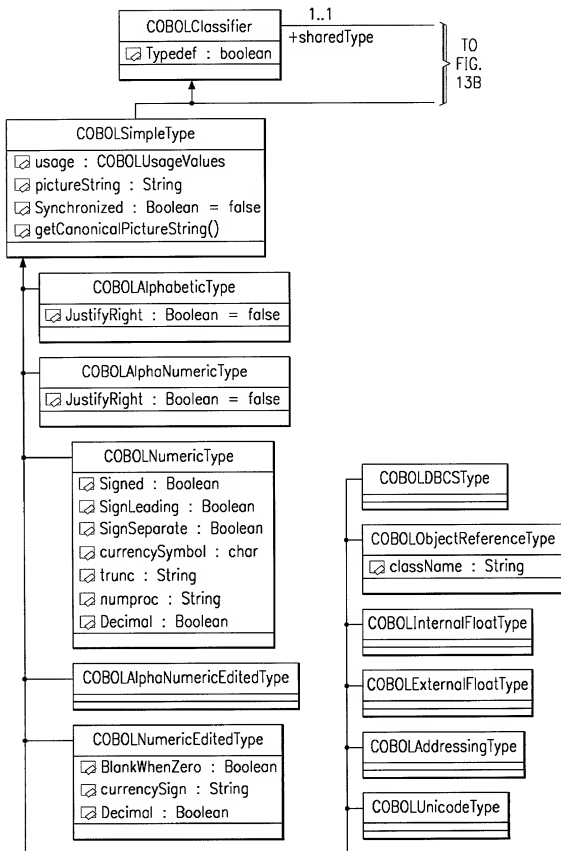


FIG. 13A



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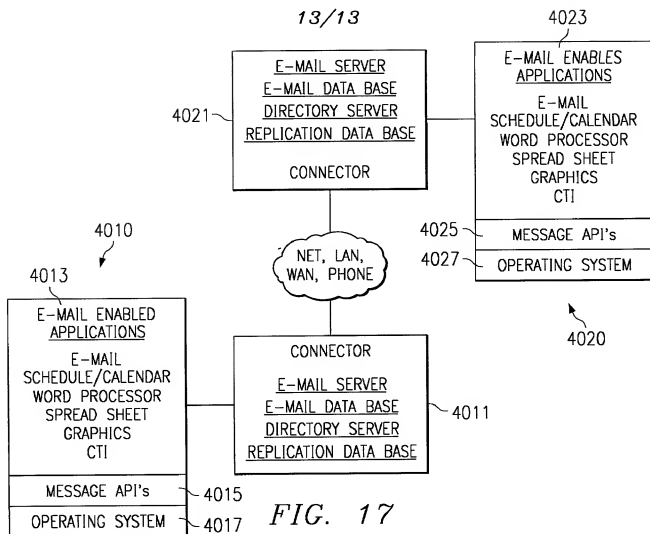


FIG. 17

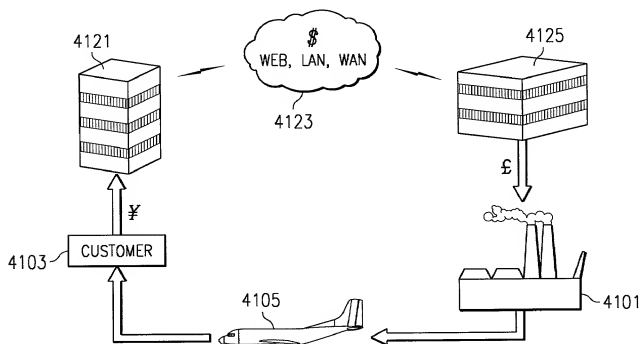


FIG. 18